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An electrical thermometry platform for measuring cross-plane thermal conductivity of 2D flakes on substrate

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ABSTRACT

It is highly desired to efficiently probe the cross-plane thermal conductivities of two-dimensional (2D) flakes with a considerably small temperature increase, avoiding the difficulty of suspending the atomically thin samples. A thermometry platform was proposed for measuring the cross-plane thermal conductivity of irregular dielectric and semiconductor 2D flakes on a substrate. Two metal heaters with identical configurations were fabricated on the same chip, one deposited on the sample and the other was directly on the substrate as a reference. The value of cross-plane thermal conductivity could be derived by fitting the heaters' effective thermal resistances with finite-element-method simulations. The measurement error of the heaters' effective thermal resistances was approximately 1% with the imposed temperature increase of less than 1 K. This platform was used to measure the cross-plane thermal conductivity of WSe₂ 2D flakes. The measured values were about eightfold smaller than those of bulk material, which agreed well with the model's predictions.

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The study of thermal transport properties of two-dimensional (2D) materials and their stacked structures (e.g., few-layer graphene and transition metal dichalcogenides) has become a research hotspot,^{1–10} owing to their great application prospects in electronics, thermoelectrics, and optoelectronics.^{11–15} The exfoliation technique has proven to be a powerful technique to produce 2D layered materials.^{16,17} However, the exfoliated samples are generally 2D flakes of small area and irregular shape, and it is rather difficult to suspend such atomically thin flakes for the measurement. These issues make the thermal property measurement of 2D layered materials very challenging.

Table I summarizes the relevant measurement techniques. As a noncontact and nondestructive technique, the Raman spectroscopy method has been widely utilized to probe the thermal properties of monolayer and few-layer 2D material flakes.^{18–21} The temperature increase within the sample is obtained by detecting the Raman shift, which exhibits a relatively poor accuracy for some materials. Thus, usually a larger temperature increase is required, which could raise concerns about the measurement precision, due to the influence of

thermal properties' variation with temperature. Beechem et al.²² discussed this issue in the Raman method in detail. Additionally, the conventional TDTR (time-domain thermoreflectance) technique has been proven to be a powerful tool with which the anisotropic thermal conductivities of bulk and thin-film materials that have a large surface area for depositing the metal transducer are probed.^{23–27} In fact, it is not well applicable for monolayer or few-layer 2D material flakes. Regarding this issue, Jang et al.28 demonstrated a modified TDTR method to characterize the anisotropic thermal conductivities of rhenium disulfide flakes with the metal transducers on their top surfaces. Except for the optical methods mentioned above, various electrical methods have been applied to characterize the thermal conductivities of layered materials. Similar to the TDTR method, the conventional 3ω technique is applicable for bulk and thin-film materials rather than 2D flakes of small area and irregular shape.^{29–31} The suspended microbridge method exhibits ultrahigh sensitivity and accuracy for in-plane thermal transport,³² and thus, it can be used to probe the in-plane thermal conductivities of 2D

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	Measurement technique	Sensitive properties	Important features
Optical	Raman	In-plane	Applicable for monolayer and ultrathin flakes; relatively poor accuracy for detecting the temperature increase
	TDTR	In/Cr-plane	Applicable for bulk and thin film materials; not well applicable for monolayer or few-layer 2D flakes
Electrical	Suspended microbridge	In-plane	High sensitivity and accuracy; applicable for in-plane thermal transport; complicated fabrication process; fragile platform
	3ω method	In/Cr-plane	Applicable for bulk and thin film materials; not applicable for 2D flakes
	Self-designed heaters	In/Cr-plane	Various designs for measuring thermal properties; usually requiring FEM simulations to extract the target properties

TABLE I. Relevant measurement techniques and their features.

layered materials, such as WSe₂, MoS₂, and ZrTe₅.^{33–35} However, the complicated fabrication process and the requirement that the sample must be suspended greatly limit the utilization of this method.³⁶ In addition, some researchers used self-designed supported heaters to measure the thermal properties of 2D materials.^{36–38} For example, Bae *et al.*³⁷ fabricated a thermometry platform to study the size-dependent in-plane thermal conductivity of supported graphene ribbons. Therefore, by properly designing the configurations, the supported heaters can be used to probe the targeted thermal properties with the assistance of finite-element-method (FEM) simulations for running the fitting process, to avoid the difficulty of suspending the atomically thin flakes.

In the present research, we designed and fabricated an electrical thermometry platform to probe the cross-plane thermal conductivity of dielectric and semiconductor 2D flakes on a substrate. Electron-beam lithography was used to define the markers on the growth substrate and then fabricate electrode patterns on selected 2D flakes on the substrate. Here, Cr/Pd (10/100 nm) films were thermally evaporated for contacts. Schematics of the platform are reproduced in Fig. 1(a). Two metal heaters with identical configurations were fabricated on the same chip. One short heater was deposited on the supported 2D flake. It was not necessary to deposit the entire heater on the 2D flake, since the sensitivity analysis demonstrated that a heater with its central part (about half of its full length) on the sample is sensitive enough to characterize the cross-plane thermal conductivity of 2D flakes. Another metal heater of the same configuration was directly fabricated on the substrate as a reference. The four-probe method was employed to measure the electrical resistance of the heaters to derive the average temperature increase. Therefore, four metal pads were necessary as shown in Fig. 1(a), and the additional two metal pads were designed for symmetry and as a double-check. Moreover, Fig. 1(b) illustrates the electrical circuit, which is identical to that in the conventional 3 ω method. We used a current source (Keithley 6221 DC and AC source) to provide the driving current of low frequency, and a lock-in amplifier (Model SR830) was used to record the 3ω voltage signals.

The heater's effective thermal resistance was calculated as

$$R_{\rm th} = \frac{\Delta T_{\rm ave}}{Q_{\rm heater}},\tag{1}$$

where ΔT_{ave} is the average temperature increase in the heater and Q_{heater} the heating power within the heater. We measured the effective thermal resistances of the reference heater and the heater on the sample, and the sample's cross-plane thermal conductivity could be derived by fitting the measured effective thermal resistances with the FEM results. For measuring the effective thermal resistance, the heating power could be easily calculated using the electric resistance and driving current, and thus, the key point accurately characterized the temperature increase. Lu *et al.*³⁹ demonstrated that the module of temperature oscillation within a conductive suspended wire with oscillating Joule heating becomes nearly the same as the temperature



FIG. 1. (a) Schematic of electrical thermometry platform; (b) corresponding electrical circuit diagram, in which R_{e0} is the heater's electric resistance, R_{ep} is the electrical resistance of a paired resistor, and DA denotes the differential amplifier.

increase in the steady-state case when the heating frequency is low enough. Thus, the heater's average temperature increase in the steady state can be derived from the measured the 3ω voltage signals ($V_{3\omega}$) with a low-frequency heating current ($I_{1\omega}$), the frequency of which is equal to 1ω ; that is,

$$\Delta T_{\rm ave} = \frac{2V_{3\omega}}{I_{1\omega}\beta R_{\rm e0}},\tag{2}$$

where β is the temperature coefficient of resistance (TCR). Although we used $V_{3\omega}$ to derive the temperature increase, the present platform basically exhibits two differences compared to the conventional 3ω method. First, its heaters' lengths should be comparable to the size of the flakes to guarantee that most of the generated heat can go through the samples, and thus, the heaters were much shorter than in the conventional 3ω method. Second, the conventional 3ω method always records a sequence of 3ω signals varying the frequency, while we merely recorded the 3ω signals at the low frequency to obtain the temperature increase at the steady state.

As a test example, we conducted the FEM simulations to determine whether the method described above was valid for our platform; the substrate was set as a SiO_2 (300 nm)/Si (450 μ m) substrate, and the cross-plane and in-plane thermal conductivities of

the sample (20 nm thick) were set as 0.1 and 10 W/m K, respectively. As illustrated in Fig. 2(a), the modules of the temperature oscillations were nearly independent of the current frequency (half of the heating frequency) and identical to the steady-state temperature increases when the reciprocal of the current frequency was higher than 0.01 s in the cases of both the reference heater and the heater on the sample.

We then analyzed the sensitivity of the present method. The sensitivity is characterized by the dimensionless sensitivity of the target parameter, and it is given by $s_i = \partial \ln (\kappa_{cr})/\partial \ln (x_i)$, in which κ_{cr} is the sample's cross-plane thermal conductivity and x_i the measured (or input) parameter. The dimensionless sensitivity was calculated numerically by giving a small perturbation of x_i around its value and rerunning the extraction simulation to derive the variation of cross-plane thermal conductivity. We calculated the dimensionless sensitivity of κ_{cr} with respect to the heater's effective thermal resistance (R_{th}), which is denoted as $|s_{R_{th}}|$. As shown in Fig. 2(b), the cross-plane thermal conductivity was found to be very sensitive to the heater's effective thermal resistance. Regardless of the value of in-plane thermal conductivity (κ_{in}), the absolute value of the dimensionless sensitivity increased with decreasing cross-plane thermal resistance of unit area, which is given by $r_{cr} = t_{sam}/\kappa_{cr}$, where t_{sam} is the sample's thickness.





Additionally, in a specific range, the variation of in-plane thermal conductivity had a minor influence on the dimensionless sensitivity with respect to the effective thermal resistance, indicating that the influence of the setting value of in-plane thermal conductivity could be negligible during the fitting process. To further demonstrate this point, we calculated the dimensionless sensitivity of κ_{cr} with respect to κ_{in} , which is denoted as $s_{\kappa_{in}}$. As shown in Fig. 2(c), the absolute value of $s_{\kappa_{in}}$ was considerably small. We could estimate the measurement range via the sensitivity analysis of $s_{R_{\rm th}}$. As $r_{\rm cr} = 2.5 \times 10^{-8} \ {\rm m}^2$ K/W and $\kappa_{\rm in} = 100$ W/m K, $|s_{R_{\rm th}}|$ reached approximately 20. We estimated that the lowest cross-plane thermal resistance of unit area that can be probed with an error of less than 50% is approximately $2.5 \times 10^{-8} \text{ m}^2 \text{ K/W}$. Nevertheless, according to Refs. 9, 37, and 40, the interfacial thermal resistance of unit area between the 2D material sample and substrate $(r_{\text{sam SiO2}})$ is on the order of $1.0 \times 10^{-8} \text{ m}^2$ K/W, and thus, $r_{\text{sam SiO2}}$ could be comparable to $r_{\rm cr}$ (the dimensionless sensitivity with respect to $r_{\text{sam}_{SiO2}}$ reached approximately 0.4). In fact, the measured cross-plane thermal conductivity should be an effective value involving the influence of interfacial thermal resistance.

We employed the present method to measure the cross-plane thermal conductivities of WSe_2 2D flakes on the SiO₂/Si substrate. High-quality layered crystals of WSe_2 were mechanically exfoliated from bulk using scotch tape (see supplementary material S.1) and then transferred onto clean silicon substrates covered with 300-nm-thick SiO₂, as shown in Figs. 3(a) and 3(b). The thicknesses of the WSe_2 flakes were 21 nm (sample 1) and 49 nm (sample 2) as measured by atomic-force microscopy (AFM) in contact mode (Fig. S1). A heater was deposited on the 2D flake, and a reference heater was also



FIG. 3. [(a) and (b)] Optical microscopy image of 21 nm and 49 nm WSe₂ electrical thermometry platforms on the SiO₂/Si substrate, respectively. (c) and (d) Blank electrical thermometry platforms, used as a reference for the measurement of 21 nm and 49 nm WSe₂, respectively.

fabricated on the same substrate, as shown in Figs. 3(c) and 3(d). Electrical contacts were fabricated using electron-beam lithography followed by deposition of 10 nm chromium/100 nm palladium electrodes (see supplementary material S.2).

The cross-plane thermal conductivities of the WSe₂ 2D flakes were derived from the measured effective thermal resistances (see supplementary material S.3) by fitting with the FEM simulation results (see supplementary material S.4). As shown in Fig. 4, at 300 K, the cross-plane thermal conductivities of the 21- and 49-nm-thick WSe₂ 2D flakes were found to be 0.194 \pm 0.026 and 0.227 \pm 0.015 W/m K, respectively (see supplementary material S.5 for error analysis), which were significantly smaller than the bulk value (approximately 1.5 W/m K),^{23,26,41} which should be attributed to the size confinement in the cross-plane direction.^{42–44} The experiments of Muratore *et al.*⁴¹ also demonstrated that the cross-plane thermal conductivity of the WSe₂ nanofilm with a turbostratic structure, the domain size of which was approximately 9 nm, can be reduced to approximately 0.1 W/m K.

A thermal conductivity model was developed in this case, and the \hat{s} directional thermal conductivity is given by

$$\kappa_{\hat{s}} = \int_{0}^{2\pi} d\varphi \int_{-1}^{1} d\mu \sum_{j} \int_{0}^{q_{mj}} \kappa_{q,j,\hat{s}} q^{2} dq,$$
(3)

with

$$\kappa_{q,j,\hat{s}} = \left(\widehat{s} \ \cdot \vec{v}_{gj}
ight)^2 au_{q,j} C_q,$$

where \vec{v}_{gj} is the group velocity, C_q the mode heat capacity, and $\tau_{q,j}$ the relaxation time (see supplementary material S.6). The model predicted cross-plane thermal conductivity values of 0.17 W/m K (21 nm) and 0.2 W/m K (49 nm) for WSe₂ at 300 K, which agreed well with our experimental results. When the scattering length was set to 9 nm, the



FIG. 4. Cross-plane thermal conductivities of WSe₂ 2D flakes vs temperature. The data are: Reproduced with permission from Muratore *et al.*, Appl. Phys. Lett. **102**(8), 081604 (2013). Copyright 2013 AIP Publishing. Reproduced with permission from Chiritescu *et al.*, Science **315**(5810), 351 (2007). Copyright 2007 The American Association for the Advancement of Science. Reproduced with permission from Jiang *et al.*, Adv. Mater. **29**(36), 1701068 (2017). Copyright 2017 John Wiley and Sons.

model could also explain the value reported by Muratore et al.⁴¹ Referring to the experimental data and model's predictions, the crossplane thermal conductivity of 2D WSe2 flakes could increase with the increasing scattering length. Nevertheless, the measured values for a 24-nm-thick disordered WSe₂ nanofilm by Chiritescu et al.²³ were only approximately 0.07 W/m K, which were much lower than our results and model predictions. This was because the disordered structure further reduced the thermal conductivity of the disordered WSe₂ nanofilm. In addition, Fig. 4 shows that our measured thermal conductivities decrease very slightly with increasing temperature. As demonstrated by previous experiments,²⁶ the Slack model,⁴¹ and our present model, the cross-plane thermal conductivities of bulk WSe2 also decreased with increasing temperature. The temperature dependence of bulk WSe2 was reduced compared to the case of 2D flakes, since the phonon-transport process within the 2D flakes should be dominated by boundary scattering.

In summary, an efficient scheme was developed for measuring the cross-plane thermal conductivity of dielectric and semiconductor 2D flakes on a substrate. A supported electrical thermometry platform was designed, and the sample's cross-plane thermal conductivity could be derived from the measured effective thermal resistances of heaters by fitting with the FEM simulation results. This measurement platform is easily fabricated and well applicable for 2D flakes of small area and irregular shape. The measurement error of the heaters' effective thermal resistances was approximately 1% with the imposed temperature increase of less than 1 K, and the lowest cross-plane thermal resistance of unit area that can be probed with a thermal conductivity uncertainty of less than 50% is approximately 2.5×10^{-8} m² K/W with an in-plane thermal conductivity of less than 100 W/m K. This method was utilized to measure the cross-plane thermal conductivity of the WSe₂ 2D flakes, and a strong boundary-scattering effect was identified. Moreover, those measured values could be well predicted by the thermal conductivity model for the thin layered materials involving the phonon-boundary-scattering effect. The present work can provide an alternative method to efficiently characterize the cross-plane thermal conductivity of dielectric and semiconductor 2D flakes on a substrate.

See the supplementary material for the details of sample preparation, fabrication of the thermometry platform, measurements, simulation of extraction of thermal conductivities, error analysis, and thermal conductivity model of thin layered materials.

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